

Docket No.: 214398US2SRD



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION: Hideto FURUYAMA

SERIAL NO.: 09/961,372

FILED: September 25, 2001

FOR: OPTICAL INTERCONNECTION CIRCUIT BOARD AND MANUFACTURING METHOD THEREOF

GAU: 2872

EXAMINER: ALLEN, D.

LETTER SUBMITTING DRAWING SHEET(S)

COMMISSIONER FOR PATENTS  
Alexandria, VA 22313

SIR:

Responsive to the below indicated communication, the following drawing sheets are submitted herewith:

☒ 2 Replacement Drawing Sheets

☐ New Drawing Sheets

☒ Official Action dated 04/19/03

☐ Notice of Allowance/Issue Fee dated \_\_\_\_\_

☐ Other dated \_\_\_\_\_

The changes and/or modifications made include the following:

The attached sheets of drawings includes changes to FIGS. 1-2 and 4. These sheets, which includes FIGS. 1-2 and 4 replace the original sheets including FIGS 1-2 and 3-4. In FIG. 4, previously presented elements n3 has been deleted and "PRIOR ART" has been added as a label to FIGS. 1-2.



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(OSMMN 05/2003)

Respectfully submitted,

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JUL 15 2003

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AUG 13 2003

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